10/554139

Express Mail No. EV514614413US

JC12 Rec'd PCT/PTO 2 0 OCT 2005

INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Attorney Docket Number	7126-72254-01
	Application Number	~~Not yet assigned~~ 11/554,139
	Filing Date	Herewith 02/01/2008
	First Named Inventor	Hammouda
	Art Unit	~~Not yet assigned~~ 2825
	Examiner Name	~~Not yet assigned~~ Lin, Sun J.

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee

FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee

Examiner's Initials*	Cite No. (optional)	OTHER DOCUMENTS
/JSL/		Dessouky, M. et al., "Analog IP Design Retargeting based on Design Extraction and Transistor Optimization," October 2004, 17 pgs.
/JSL/		Hammouda, S. et al., "A Fully Automated Approach for Analog Circuit Reuse," July 2004, 4 pgs.

EXAMINER SIGNATURE:	/Sun J. Lin/	DATE CONSIDERED:	05/11/2009

* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.

Information Disclosure Statement (1449) Page 1 of 1